
Microprocessor Book By Bhurchandi 1239 dalglor

IRMC New Releases) The usage of the C language on the computer systems has become increasingly popular in recent years. It is because of C's clean syntax, concise semantics and efficient performance. The syntax of C is relatively simple and straightforward. By Master Author # Copyright, 1996 This book contains its own source code for Chapter 6. Public domain, do not edit # Programming - The GNU C Programming Language Chapter 5 - No High Priority Interrupts No High Priority Interrupts The High Priority IRQ is used to take immediate action when the system detects an error or when a defined system condition occurs. If the task does not need to execute immediately, the processor may simply execute the task with the normal interrupt and context processors. However, when the immediate interrupt is required, the processor will perform the task with the high priority interrupt or interrupt service routine (IRQ). The high priority interrupt can be turned on using the the HAL_set_irq_enable() function. The HAL_set_irq_enable() function is defined in the HAL_api.c file. The HAL_set_irq_enable() function is used to set the interrupt enable bit for the given interrupt number. The HAL_clear_irq_enable() function is used to reset the interrupt enable bit for the given interrupt number. The HAL_set_irq_enable() function sets the enabled state of the given interrupt number. The interrupt number is specified in the interrupt number parameter. The HAL_set_irq_enable() function sets the high priority interrupt enable for the given interrupt number. The HAL_clear_irq_enable() function clears the enabled state of the given interrupt number. The interrupt number is specified in the interrupt number parameter. uint32_t HAL_set_irq_enable(uint32_t irq_number, uint32_t enable); Parameters: irq_number: The interrupt number enable: A value to set the interrupt enable for the given interrupt Function: void HAL_clear_irq_enable(uint32_t irq_number); HAL_clear_irq_enable() is used to set the interrupt enable bit to 0 for the given interrupt number. The interrupt number is specified in the interrupt number parameter. The HAL_set_irq_enable

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